DYC Ref: P17073US ARM Ref: P302

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## ABSTRACT OF THE DISCLOSURE

A data processing apparatus 2 is provided which is responsive to data access instructions to perform data access operations. These data access instructions have a first form utilising a 12-bit offset field but with a fixed addressing mode and a second form utilising a shorter 8-bit offset field but with an addressing mode specified within a manipulation mode control field of the data access instruction.

[Figure 4]